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	L4	(power near2 ramp\$4) and (pilot\$) and (preamble\$)	41
	L3	(synch\$7 near3 channel\$) and (power near2 ramp\$4) and (pilot\$) and (preamble\$)	0
	L2	(common near2 synch\$7 near3 channel\$) and (power near2 ramp\$4) and (pilot\$) and (preamble\$)	1
	L1	(common near2 synch\$7 near3 channel\$) same (power near2 ramp\$3) same (pilot\$) same (preamble\$)	0

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# **Inventor Information for 10/667665**

Inventor Name	City		State/Country				
KANTERAKIS, EMMANUEL	NORTH BRUNSWI	CK	NEW JERSEY				
PARSA, KOUROSH	RIVERSIDE		CONNECT	ICUT			
Appln Info   Contents   Petition Info	Atty/Agent Info	Continuity	Data	Foreign Data			
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#### **Inventor Name Search Result**

Your Search was:

Last Name = KANTERAKIS First Name = EMMANUEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name .
08068518	5515194	150	05/28/1993	OPTICAL INTERCONNECT FOR HIGH SPEED PROCESSORS	KANTERAKIS, EMMANUEL
08536749	5719898	150	09/29/1995	FUZZY-LOGIC SPREAD-SPECTRUM ADAPTIVE POWER CONTROL	KANTERAKIS, EMMANUEL
08564007	5872808	150	11/29/1995	APPARATUS AND METHOD FOR SYNCHRONIZATION OF DIRECT SEQUENCE CDMA SIGNALS	KANTERAKIS, EMMANUEL
08578422	5640425	150	12/26/1995	FAST-ACTING COSTAS LOOP	KANTERAKIS, EMMANUEL
08634160	5757791	150	04/18/1996	MULTISTAGE LINEAR RECEIVER FOR DS-CDMA SYSTEMS	KANTERAKIS, EMMANUEL
08641632	5677778	150	05/02/1996	OPTICAL INTERCONNECT FOR HIGH SPEED PROCESSORS	KANTERAKIS, EMMANUEL
08700012	5742637	150	08/20/1996	FAST PHASE ESTIMATION IN DIGITAL COMMUNICATION SYSTEMS	KANTERAKIS, EMMANUEL
08842527	6154483	150	04/07/1997	COHERENT DETECTION USING MATCHED FILTER ENHANCED SPREAD SPECTRUM DEMODULATION	KANTERAKIS, EMMANUEL
08873253	5956375	150	06/11/1997	FAST-ACTING COSTAS LOOP	KANTERAKIS, EMMANUEL
08896367	6008918	150	07/18/1997	OPTICAL INTERCONNECT FOR HIGH SPEED PROCESSOR	KANTERAKIS, EMMANUEL
08969343	6061359	150	11/28/1997	11	KANTERAKIS, EMMANUEL

				SYSTEM AND METHOD	
09005071	6212244	150	01/09/1998	FAST RESPONSE AUTOMATIC GAIN CONTROL	KANTERAKIS, EMMANUEL
09007026	5963583	150	01/14/1998	FUZZY-LOGIC SPREAD-SPECTRUM ADAPTIVE POWER CONTROL	KANTERAKIS, EMMANUEL
09016324	6021157	150	01/30/1998	FAST PHASE ESTIMATION IN DIGITAL COMMUNICATION SYSTEMS	KANTERAKIS, EMMANUEL
09083193	6130906	150	05/22/1998	PARALLEL CODE MATCHED FILTER	KANTERAKIS, EMMANUEL
09157300	6041073	150	09/18/1998	MULTI-CLOCK MATCHED FILTER FOR RECEIVING SIGNALS WITH MULTIPATH	KANTERAKIS, EMMANUEL
09182054	6393049	150	10/29/1998	TWO-STAGE SYNCHRONIZATION OF SPREAD-SPECTRUM SIGNALS	KANTERAKIS, EMMANUEL
09224722	6587452	150	01/04/1999	HIGH PERFORMANCE SIGNAL STRUCTURE WITH MULTIPLE MODULATION FORMATS	KANTERAKIS, EMMANUEL
09273450	6574267	150	03/22/1999	RACH RAMP-UP ACKNOWLEDGEMENT	KANTERAKIS, EMMANUEL
09273507	6324207	150	03/22/1999	HANDOFF WITH CLOSED-LOOP POWER CONTROL	KANTERAKIS, EMMANUEL
09273508	6169759	150	03/22/1999	COMMON PACKET CHANNEL	KANTERAKIS, EMMANUEL
09275010	6389056	150	03/24/1999	PRE-DATA POWER CONTROL COMMON PACKET CHANNEL	KANTERAKIS, EMMANUEL
09304345	6606341	150	05/04/1999	COMMON PACKET CHANNEL WITH FIRMWARE HANDOFF	KANTERAKIS, EMMANUEL
09329640	Not Issued	161	06/10/1999	ADAPTIVE POWER CONTROL	KANTERAKIS, EMMANUEL
09344085	6122328	150	06/25/1999	FAST-ACTING COSTAS LOOP	KANTERAKIS, EMMANUEL
09430543	6243180	150	10/29/1999	OPTICAL INTERCONNECT FOR HIGH SPEED PROCESSORS	KANTERAKIS, EMMANUEL

<u>09466797</u>	6515981	150	12/20/1999	PACKET-SWITCHED SPREAD-SPECTRUM SYSTEM	KANTERAKIS, EMMANUEL
09526867	6631157	150	03/16/2000	MULTI-CLOCK MATCHED FILTER FOR RECEIVING SIGNALS WITH MULTIPATH	KANTERAKIS, EMMANUEL
09565049	Not Issued	161	05/05/2000	FAST RESPONSE AUTOMATIC GAIN CONTROL	KANTERAKIS, EMMANUEL
<u>09580976</u>	Not Issued	161	05/30/2000	FAST RESPONSE AUTOMATIC GAIN CONTROL	KANTERAKIS, EMMANUEL
09605508	6304592	150	06/28/2000	COHERENT DETECTION USING MATCHED FILTER ENHANCED SPREAD SPECTRUM DEMODULATION	KANTERAKIS, EMMANUEL
09605516	Not Issued	161	06/28/2000	PARALLEL CODE MATCHED FILTER	KANTERAKIS, EMMANUEL
09679367	6301286	150	10/05/2000	COMMON PACKET CHANNEL	KANTERAKIS, EMMANUEL
09695720	6643318	150	10/25/2000		KANTERAKIS, EMMANUEL
09722685	6757319	150	11/28/2000	il .	KANTERAKIS, EMMANUEL
09722688	6480525	150	11/28/2000	SECOND LEVEL COLLISION RESOLUTION FOR PACKET DATA COMMUNICATIONS	KANTERAKIS, EMMANUEL
09778955	6507601	150	02/08/2001	COLLISION AVOIDANCE	KANTERAKIS, EMMANUEL
09847180	6373605	150	05/02/2001	OPTICAL INTERCONNECT FOR HIGH SPEED PROCESSOR	KANTERAKIS, EMMANUEL
09941756	6717975	150	08/30/2001	COMMON PACKET CHANNEL	KANTERAKIS, EMMANUEL
10067015	6426818	150	02/04/2002	OPTICAL INTERCONNECT FOR HIGH SPEED PROCESSOR	KANTERAKIS, EMMANUEL
10096312	6639936	150	03/13/2002	PRE-DATA POWER	KANTERAKIS,

Construction				CONTROL COMMON PACKET CHANNEL	EMMANUEL
10144749	Not Issued	030	05/15/2002	CHANNEL CAPACITY OPTIMIZATION FOR PACKET SERVICES	KANTERAKIS, EMMANUEL
10162262	6563617	150	06/04/2002	OPTICAL APPARATUS USING A 2D-SPATIAL LIGHT MODULATOR	KANTERAKIS, EMMANUEL
10379620	6711358	150	03/05/2003	OPTICAL APPARATUS USING A PHASE GRATING	KANTERAKIS, EMMANUEL
10412576	Not Issued	030	04/14/2003	COLLISION DETECTION	KANTERAKIS, EMMANUEL
10607342	Not Issued	020	06/27/2003	COMMON PACKET CHANNEL WITH FIRM HANDOFF	KANTERAKIS, EMMANUEL
10649816	Not Issued	030	08/28/2003	PRE-DATA POWER CONTROL COMMON PACKET CHANNEL	KANTERAKIS, EMMANUEL
10667665	Not Issued	030	09/23/2003	COMMON PACKET CHANNEL	KANTERAKIS, EMMANUEL
10678648	Not Issued	030	10/06/2003	ENHANCED UPLINK PACKET TRANSFER	KANTERAKIS, EMMANUEL
10682985	Not Issued	041	10/14/2003	HYBRID DSMA/CDMA (DIGITAL SENSE MULTIPLE ACCESS/ CODE DIVISION MULTIPLE ACCESS) METHOD WITH COLLISION RESOLUTION FOR PACKET COMMUNICATIONS	KANTERAKIS, EMMANUEL

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#### **Inventor Name Search Result**

Your Search was:

Last Name = PARSA First Name = KOUROSH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09273450	6574267	150	03/22/1999	RACH RAMP-UP ACKNOWLEDGEMENT	PARSA, KOUROSH
09273507	6324207	150	03/22/1999	HANDOFF WITH CLOSED-LOOP POWER CONTROL	PARSA, KOUROSH
09273508	6169759	150	03/22/1999	COMMON PACKET CHANNEL	PARSA, KOUROSH
09275010	6389056	150	03/24/1999	PRE-DATA POWER CONTROL COMMON PACKET CHANNEL	PARSA, KOUROSH
09304345	6606341	150	05/04/1999	COMMON PACKET CHANNEL WITH FIRMWARE HANDOFF	
<u>09679367</u>	6301286	150	10/05/2000	COMMON PACKET CHANNEL	PARSA, KOUROSH
09695720	6643318	150	10/25/2000	HYBRID DSMA/CDMA (DIGITAL SENSE MULTIPLE ACCESS/CODE DIVISION MULTIPLE ACCESS) METHOD WITH COLLISION RESOLUTION FOR PACKET COMMUNICATIONS	PARSA, KOUROSH
09722685	6757319	150	11/28/2000	CLOSED LOOP POWER CONTROL FOR COMMON DOWNLINK TRANSPORT CHANNELS	PARSA, KOUROSH
09722688	6480525	150	11/28/2000	SECOND LEVEL COLLISION RESOLUTION FOR PACKET DATA COMMUNICATIONS	PARSA, KOUROSH
09778955	6507601	150	02/08/2001	COLLISION AVOIDANCE	PARSA, KOUROSH
09941756	6717975	150	08/30/2001	COMMON PACKET CHANNEL	PARSA, KOUROSH
10096312	6639936	150		PRE-DATA POWER CONTROL COMMON PACKET CHANNEL	:1

10412576	Not Issued	030	04/14/2003	COLLISION DETECTION	PARSA, KOUROSH
10607342	Not Issued	020	06/27/2003	COMMON PACKET CHANNEL WITH FIRM HANDOFF	PARSA, KOUROSH
10649816	Not Issued	030	08/28/2003	PRE-DATA POWER CONTROL COMMON PACKET CHANNEL	PARSA, KOUROSH
10667665	Not Issued	030	09/23/2003	COMMON PACKET CHANNEL	PARSA, KOUROSH
10682985	Not Issued	041	10/14/2003	HYBRID DSMA/CDMA (DIGITAL SENSE MULTIPLE ACCESS/ CODE DIVISION MULTIPLE ACCESS) METHOD WITH COLLISION RESOLUTION FOR PACKET COMMUNICATIONS	PARSA, KOUROSH
60161443	Not Issued	159	10/26/1999		PARSA, KOUROSH
60167852	Not Issued	159	11/29/1999	CLOSED LOOP POWER CONTROL OF THE FORWARD ACCESS CHANNEL (FACH), DOWNLINK COMMON PACKET CHANNEL VIA UPLINK CPCH	A THE CONTRACTOR AND A SECOND
60181200	Not Issued	159	02/09/2000	CHANNEL ASSIGNMENT AND UE CHANNEL SELECTION METHOD	PARSA, KOUROSH
60237400	Not Issued	159	10/04/2000	CAPACITY OPTIMIZATION OF W-CDMA FOR PACKET DATA SERVICES	PARSA, KÖUROSH
60239279	Not Issued	159	10/12/2000	OPTIMIZATION OF THE PHYSICAL DEDICATED CHANNEL IN W-CDMA FOR PACKET DATA SERVICES	PARSA, KOUROSH
60262033	Not Issued	159		IMMEDIATE UPLINK (UL) DEDICATED CHANNEL (DCH) RELEASE MECHANISMS IN DS-SS CDMA SYSTEMS	PARSA, KOUROSH
60290642	Not Issued	159	05/15/2001	CHANNEL CAPACITY OPTIMIZATION FOR PACKET SERVICES	PARSA, KOUROSH

60290	Not Issued	159		INTERFERENCE CONTROL AT CELL EDGE THROUGH LIMITING DATA RATE AND TRANSMISSION POWER	PARSA, KOUROSH		
Inventor Search Completed: No Becorde to Dignley							

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	L4	(power near2 ramp\$4) and (pilot\$) and (preamble\$)	41
	L3	(synch\$7 near3 channel\$) and (power near2 ramp\$4) and (pilot\$) and (preamble\$)	0
	L2	(common near2 synch\$7 near3 channel\$) and (power near2 ramp\$4) and (pilot\$) and (preamble\$)	1
	L1	(common near2 synch\$7 near3 channel\$) same (power near2 ramp\$3) same (pilot\$) same (preamble\$)	0

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